

REMARKS

Claims 1-33 are pending in this application. Applicant wishes to thank the Examiner for the allowance of claims 1-33. During a review of the specification, it was noted that equations 33, 34, and 35 on pages 22 and 23 of the present specification were omitted during translation. In the priority document, filed with the present application, the missing symbols are included as shown in paragraph 49 of the Japanese publication attached hereto for the Examiner's convenience. Further, it should be noted that from the text it is clear that equation 33 is the adder which is replaced with a multiplier and that the four input comparing selecting circuit is also modified with an adder so that the resulting equation is as discussed on pages 23, et seq. of the present specification.

Applicants respectfully request inclusion of the equations in the publication of the specification. No new matter has been added as discussed herein and as shown in the priority document.

If it is determined that a telephone conference would expedite the prosecution of this application, the Examiner is invited to telephone the undersigned at the number given below.

In view of the above, this application is believed to be in immediate condition for allowance. Accordingly, the Examiner is respectfully requested to withdraw the outstanding rejection to pass this application to issue.

If the Examiner believes an interview would be of assistance, the Examiner is welcome to contact the undersigned at the number listed below.

Dated: June 6, 2006

Respectfully submitted,

By 

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